

**Amendments to the Specification:**

Please replace the title on page 1, line 4 with the following amended title:

**METHOD AND APPARATUS FOR POSTING A COMPUTER HAVING  
MULTIPLE VIDEO ADAPTERS**

Please replace the paragraph beginning on page 5, line 29, with the following amended paragraph:

It is also possible to have a configuration, wherein the laptop computer has a dual display feature. In fact[[ ]], laptop computers that have the ATI RAGE <sup>TM</sup> MOBILITY 128 graphics chip, ~~manufacture~~ manufactured by ATI Technologies Inc., has a Tri-View architecture that allows the video output to be directed to any three of the LCD, TMDS, CRT and TV displays simultaneously. Dual display makes use of a second, independent controller within the graphics chip to display different images on any two of its supported outputs. This is the feature which is supported by Windows 98 operating systems, but not by Windows 2000 operating systems.

Please replace the paragraph beginning on page 6, line 17, with the following amended paragraph:

FIG. 2 schematically depicts the POST of a computer system which has a motherboard 200 with a ROM 202 which contains the system BIOS 208 and the video BIOS in compressed form for a first video adapter 203. The computer system also has an add-on card 204, which has a ROM 206 that has an uncompressed video BIOS for a video adapter 207 on the video card 204. During POST the System BIOS 208 on the motherboard 200 decompresses the motherboard video BIOS and stores it in a first memory location 210 in system memory 212. When the video adapter 203 on the

motherboard 200 is a secondary video adapter and the second video adapter on the add-on video card 204 is the primary video adapter, the System BIOS or Video BIOS 208 then copies the uncompressed and POSTed video BIOS for the first video adapter 203 on the motherboard into second memory area 214 (i.e. frame buffer) [[..]]. Although the uncompressed video BIOS of the motherboard can be copied to other locations in the system memory, the frame buffer is easily addressed by the video system of the computer. The uncompressed video BIOS of the add-on video card 204 is then copied and stored in the first memory area 210, which overwrites the video BIOS of the motherboard which was stored there. The video driver for the video adapter 203 on the motherboard 200 can now copy the uncompressed Video BIOS from the motherboard from the [[()]]second memory area 214 into a third memory area 216 where it can be utilized for ongoing operation of the computer system. It should be understood that the present invention can be extended so that second and third add-on video cards, for example, could be utilized in the computer system. Although the embodiment of the present invention is depicted in the setting of a laptop computer connected to a docking station, other various configurations and combinations of computer equipment and add-on video cards could also be utilized with the present invention.

Please replace the paragraph beginning on page 7, line 19, with the following amended paragraph:

If, in step 308, it is determined that the first video adapter on the motherboard is the primary video adapter, then the POST continues on to finish in step 320. If, however, the first video is the secondary video adapter, then the uncompressed first video BIOS is copied from the first memory area into the second memory area ( i.e. frame buffer) in

step 312. The frame buffer is the portion of memory reserved for holding the complete bit-mapped image that is sent to the display device. In step 314 the second video BIOS is stored in the first memory area of system memory, thereby overwriting the previously stored first video BIOS. In step 316 the POST operation continues for the second video BIOS on the add-on video card. In step 318, the first video BIOS is copied by the driver from the second memory area, i.e. frame buffer, to a third memory area of the system memory for use by the video driver for the first video adapter on the motherboard.

Finally, the POST goes on to finish in step 320.

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